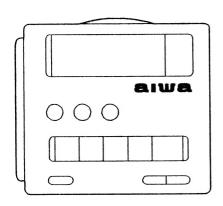
alwa



AM-F3



MINI DISC RECORDER

BASIC MD MECHANISM: 3ZG-5

• TYPE: AE1.AEH1

SPECIFICATIONS

Main unit
Playback system
Laser pickup
Recording system

MiniDisc digital audio system Semiconductor laser Magnetic polarity modulation overwrite system Approx. 400 to 900 rpm (CLV)

Revolutions

Sampling Frequency 44.1 kHz Number of channels Stereo: 2 channels Monaural: 1 channel Eight to Fifteen Modulation (EFM) 1-bit Modulation system

A/D, D/A converter

Frequency response 20 to 20,000 Hz ±1dB Wow and Flutter Below measurable limit Below measurable limit (±0.001% W.PEAK)

	MIC.	LINE IN*	OPTICAL (digital) IN
Jack type	Stereo mini- jack	Stereo mini- jack	OPTICAL mini-jack
Rated input level	1.0 mV	250 mV	_
Minimum input level	0.5 mV	125 mV	

* MIC or LINE IN input is selectable by a slide switch.

Output

	PHONES	LINE OUT
Jack type	Stereo mini-jack	Stereo mini-jack
Rated output level	-	250mV
Maximum output level	10 mW+10 mW	_
Load impedance	16 Ω	10 kΩ

Power requirements

· Rechargeable battery: Lithium-ion battery LIP-12 (supplied), DC 3.6 V Dry cell battery: LR6 (size AA) alkaline battery × 2, DC 3 V AC adaptor AC-421

(supplied) connected to the DC 4.5V jack

Car battery with the use of the car battery adaptor DC-450 (not supplied) DC12V/

Battery life

	Continuous playback time	Continuous recording time
With rechargeable battery*	Approx. 6.0 hours	Approx. 3.5 hours
With dry cell batteries	Approx. 4.0 hours	Approx. 1.0 hour
* When the recha		
Dimensions	× 81.8 (D) mi	3 (W) × 26.5 (H) T1 (4 ³/s × 1 ¹/16 ×
	3 1/4 inches) i	ncl. battery cover
Weight	Approx. 290	
-	incl. recharge	able battery.

- Design and specifications are subject to change without
- US AND FOREIGN PATENTS LICENSED FROM DOLBY LABORATORIES LICENSING CORPORATION.

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PROTECTION OF EYES FROM LASER BEAM DURING SERVICING

This set employs laser. Therefore, be sure to follow carefully the instructions below when servicing.

WARNING!

WHEN SERVICING, DO NOT APPROACH THE LASER EXIT WITH THE EYE TOO CLOSELY. IN CASE IT IS NECESSARY TO CONFIRM LASER BEAM EMISSION. BE SURE TO OBSERVE FROM A DISTANCE OF MORE THAN 30cm FROM THE SURFACE OF THE OBJECTIVE LENS ON THE OPTICAL PICK-UP BLOCK.



- Caution: Invisible laser radiation when open and interlocks defeated avoid expo-sure to beam.
- Advarsel: Usynlig laserståling ved åbning, når sikkerhedsafbrydere er ude af funktion.
 Undgå udsættelse for stråling.

VAROITUS!

Laiteen Käyttäminen muulla kuin tässä käyttöohjeessa mainit-ulla tavalla saattaa altistaa käyt-täjän turvallisuusluokan 1 ylit-tävälle näkymättömälle lasersäteilylle.

VARNING!

Om apparaten används på annat sått ån vad som specificeras i denna bruksanvising, kan användaren utsättas för osynling laserstrålning, som överskrider gränsen för laserklass 1.

CAUTION

Use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous radi-ation exposure.

ATTENTION

L'utilisation de commandes, réglages ou procédures autres que ceux spécifiés peut entraîner une dangereuse exposition aux radiations.

ADVARSEL!

Usynlig laserståling ved åbning, når sikkerhedsafbrydereer ude af funktion. Undgå udsættelse for stråling.

This Compact Disc player is classified as a CLASS 1 LASER product.

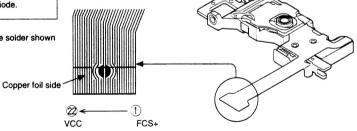
The CLASS 1 LASER PRODUCT label is located on the rear exterior.



Precaution to replace Optical block (KSM-194C)

Body or clothes electrostatic potential could ruin laser diode in the optical block. Be sure ground body and workbench, and use care the clothes do not touch the diode.

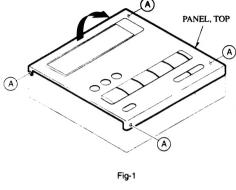
 After the connection, remove solder shown in the right figure.

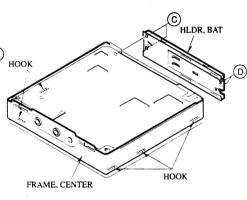


REFERENCE NAME LIST.....

DISASSEMBLY INSTRUCTIONS

- 1. PANEL, TOP removal (Refer to Fig-1.)
- 1) Remove the four screws ((A)) and remove the PANEL,
- 3. FRAME, CENTER removal (Refer to Fig-3.)
 - Remove the two screws (©) remove the two screws (©)
 and remove the HLDR, BAT.
 - Remove the FRAME, CENTER paying attention not to damage the hooks.





2. PANEL, BOT removal (Refer to Fig-2.)

1) Remove the four screws (®) and remove the PANEL, ROT

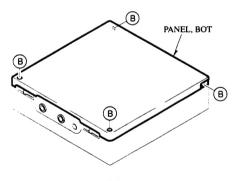


Fig-2

4. CHAS ASSY, TOP removal (Refer to Fig-4.)

Fig-3

 Remove the screw (E), remove the two screws (E) and remove the CHAS ASSY, TOP in the direction of arrow.

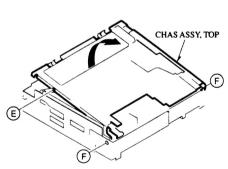


Fig-4

5. MAIN board removal (Refer to Fig-5.)

1) Remove the seven screws (⑤), remove the screw (円) and remove the screw (년). Remove the connectors (CN200, CN100 and CN500) and remove the MAIN board.

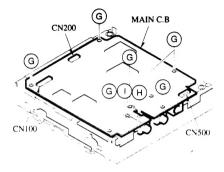


Fig-5

6. MD mechanism removal (Refer to Fig-6.)

 Remove the three screws (①) and remove the MD mechanism in the direction of arrow.

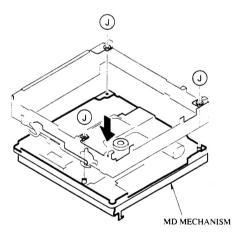


Fig-6

7. How to remove the PICK UP ASSY

1) Remove the two screws (K). (Refer to Fig-7.)

Note: Do not remove the screws with star mark *.

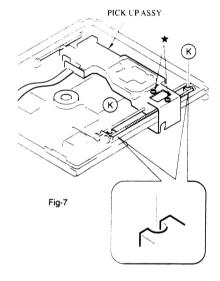
 While lifting up the over-write head arm in the direction of (1), pull out the PICK UP ASSY in the direction of arrow (2). (Refer to Fig. 8.)

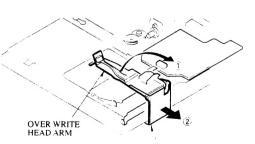
Note: Do not take hands from the over-write head arm after the PICK UP ASSY is removed, because the arm will hit the PICK UP lens and damage it.

> Insert a sheet of paper between the lens and over-write head are to prevent the lens from damage so that the lens will not touch the over-write head arm directly, when placing the PICK UP ASSY on a work bench.

8. How attach the PICK UP ASSY

 Do not tighten the screw (K) too strongly. (There may be a case that movement of PICK UP ASSY does not move smoothly.) Apply adhesive agent to lock the tightened screw.)





PICK UP ASSY

Fig-8

		escription please kindly refer to											
NO.	PART NO. 5:	DESCRIPTION	REF. NO	PART NO.	カンリ NO.	DESCRIPTION			C665 C700	87-012-286-080 87-010-831-080	C-CAP,U 0.01-25 B C-CAP,U 0.1-16F		L601
	M	,	C112	87-012-286-080		U 0.01-25 B			C900	87-016-296-080 87-A10-047-080	C-CAP, TN 22-4 SV(A) C-CAP, U 1-10 Z P		L90
		anget at the set at 1 min.	C114	87-016-296-080	C-CAP. 7	TN 22-4 SV(A)			C902	87-A10-047-080	C-CAP,U 1-10 Z F		L903
8	7-A20-014-010 7-017-857-080	C-IC, CXA1981Q/AR	C115	87-012-286-080 87-016-296-080 87-210-048-080	C-000	U 0.01-25 B TH 22-4 SV(A) TH 22-4 J95 B			C903 C904	87-A10-047-080 87-A10-047-080	C-CAP,U 1-10 Z F C-CAP,U 1-10 Z F	45	L904
87-	A20-013-010	C-IC, CC 2535BR C-IC, TC75004PU	C117	87-X10-048-080	C-CAP.1	TH 22-4 J75 8			C906	87-016-436-080 87-012-198-080	C-CAP, TN 47-4(B2) C-CAP, U 180P-50 CH		R418
	7-A20-012-010	C-IC,CXD2536R	C118 C119	87-012-195-080 87-A10-048-080		U 100P-50 CH TN 22-4 F95 S			C907 C908	87-012-198-080	C-CAP, TN 22-4 SV(A)		R60
	-017-986-010	IC,HM51W4400BLTT-8	C120	87-010-787-080	C-CAP,	U 0.022-25B			C909	87-016-296-080	C-CAP, TN 22-4 SV(A)		R60
	-017-872-080 -HM1-601-210	IC,TC7W08FU IC,CXP81848	C121 C123	87-010-829-080 87-010-787-080		U 0.047-16F U 0.022-25B			C910 C911	87-012-172-080 87-012-186-080			R60 R62
87	-A20-009-040 -070-349-040	C-IC, MPC17A38VMEL IC, LB1635M	C124	87-A10-047-080	C-CAP,	U 1-10 Z F			C912	87-012-198-080	C-CAP,U 180P-50 CH		R63 SW1
			C125	87-012-286-080 87-A10-047-080	C-CAP,	U 0.01-25 B U 1-10 Z F			C913	87-012-172-080	C-CAP,U 10P-50 CH		
	-017-869-040 -070-296-080	IC,MB3776 IC,RS5RM3027A	C127 C200	87-A10-047-080	C-CAP,	U 1-10 Z F			C914 C915	87-012-186-080 87-016-296-080			SW2 SW9
	017-747-080 017-862-080	IC,TC7W04FU IC,TC7S08FU	C201	87-012-279-080	C-CAP,	U 2700P-50 B			C916	87-A10-019-080	C-CAP, TN 10-6.3 A		SW! VR:
	070-299-040	IC.YSS231M	C202 C204	87-012-195-080 87-010-196-080	C-CAP,	U 100P-50 CH S 0.1-25 F			C917 C918	87-A10-019-080 87-A10-047-080			VR
87-	A20-008-040	C-IC,74HC368PW	C205	87-010-197-080	C-CAP.	S 0.01-25 B			C919	87-A10-047-080	C-CAP,U 1-10 Z F	,	VR
	HM1-622-040 A20-210-010	IC,CXA8027N C-IC,TDA1309HN2	C206 C208	87-A10-047-080 87-010-831-080		U 1-10 Z F U 0.1-16F			C920	87-016-449-080	C-CAP, TN 10-4 F95 P		X3 X4
87-A	20-140-040	C-IC, NJM3414V	C209	87-016-296-080		TN 22-4 SV(A)			C921 C922	87-016-296-080 87-A10-047-080	C-CAP,U 1-10 Z F		x
	7-017-861-040	IC,TC7W74FU	C210	87-012-196-080	C-CAP,	U 120P-50 CH			C923	87-012-286-080	C-CAP,U 0.01-25 B		
	37-A20-143-040 37-017-596-080	C-IC, TK11228 IC, NJN2107F	C212 C213	87-010-787-080 87-012-276-080	C-CAP,	U 0.022-25B U 1500P-50 B			C925 C927	87-016-296-080 87-A10-047-080	C-CAP, TN 22-4 SV(A) C-CAP, U 1-10 Z F		ME
8	37-017-853-040 37-017-855-040	IC,NJM2100V IC,BH3530FS	C214	87-A10-047-080	C-CAP,	U 1-10 Z F			C928	87-A10-047-080	C-CAP,U 1-10 Z F		
	7-017-863-040	IC, TC4W66FU	C300	87-016-296-080		TN 22-4 SV(A)			C929 C930	87-A10-047-080 87-012-170-080			,
			C301 C303	87-016-296-086 87-010-831-086	C-CAP,	TN 22-4 SV(A) U 0.1-16F			C931	87-012-170-080			
)R			C304 C305	87-012-170-080 87-012-170-080		U 8P-50 CH U 8P-50 CH			C932	87-A10-047-080	C-CAP,U 1-10 Z F		
	-026-647-080	C-TR, DTA123JE							C933 C934	87-A10-047-080 87-016-449-080	C-CAP, TN 10-4 F95 P		
	15-884-080 30-027-080	C-TR, 2SA1588Y C-TR, 2SC4617R	C400 C401	87-012-169-080 87-012-170-080	C-CAP,	U 7P-50 CH U 8P-50 CH			C935	87-012-275-080	C-CAP,U 1200P-50 B		
	026-644-080 424-022-080	C-TR, DTA144EE C-TR, 2SD2402EY	C402 C403	87-010-831-080 87-010-831-080		U 0.1-16F U 0.1-16F			C936	87-012-275-080			
	26-653-080	C-TR, DTC123JE	C405	87-A10-035-08		TN 2.2-4 SVS-P			C937 C938	87-A10-047-080 87-A10-047-080	C-CAP,U 1-10 Z F		
87-026	-645-080	C-TR, DTC144EE	C406 C407	87-012-286-086 87-012-286-086		U 0.01-25 B U 0.01-25 B			C940 C942	87-016-296-080 87-016-562-080			
89-341	-713-080 -165-080	C-TR, 2SB1571FZ C-TR, 2SC4116GR	C408	87-012-286-08	C-CAP,	U 0.01-25 B			C943	87-A10-193-080	C-CAP, 4.7-16 Z F		
89-	112-134-080	C-TR, 2SA1213Y	C500 C501	87-010-831-08 87-A10-047-08		U 0.1-16F U 1-10 Z F			C944 C945	87-A10-193-080 87-016-446-080	C-CAP, 4.7-16 Z F		
	9-523-150-080 7-A30-030-080	C-TR, 2SK2315 C-TR, RN2911	C502	87-016-432-08	C-CAP.	TN 22-6.3 F95A			C946	87-016-446-080	C-CAP, TN 220-4 F95 G		М
1	37-026-527-080	C-TR, HN1A01FU(G)	C503	87-A10-047-08 87-A10-047-08	C-CAP,	U 1-10 Z F			C947	87-012-191-080	C-CAP,U 68P-50 CH		
	89-115-864-080 87-026-529-080	C-TR, 2SA1586Y C-TR, RN1907	C504 C507	87-016-296-08	0 C-CAP,	U 1-10 Z F TN 22-4 SV(A)	((C948 C949	87-016-296-080 87-010-831-080			
	87-A30-035-080	C-TR. DTC323TU	C509	87-016-564-08	O C-CAP,	,10-6.3 PSN			C950	87-012-186-080	C-CAP, U 39P-50 CH		
			C509 C510	87-A10-057-08 87-016-432-08	C-CAP	TN 100-4 F95 E TN 22-6.3 F95A			C951 C952	87-012-186-080 87-012-191-080			
			C601	87-016-442-08	CAP, AS	S 33-10 OS SL	•		CN100	87-A60-088-080	C-CONN, 22P CPP55		
	37-017-850-080	C-DIODE, DAP222	C603 C604	87-A10-047-08 87-012-274-08	O C-CAP,	U 1-10 2 P U 1000P-50 B			CN200 CN300	87-A60-092-080 87-A60-090-080	C-CONN, 11P CFP55		
	37-A40-124-080 37-A40-158-080	C-DIODE, RB501V-40 C-DIODE, SB02-03C	C605	87-012-284-08	O C-CAP	Ú 6800P-50 B			CN400	87-A60-091-080	C-CONN, 12P CFP55		
	87-017-852-080 87-017-753-080	C-DIODE, HRW0502A C-DIODE, SB007T03Q	C606 C607	87-012-276-08 87-012-276-08		U 1500P-50 B U 1500P-50 B			CN500	87-A60-128-080			
			C608	87-A10-035-08	0 C-CAP	TN 2.2-4 SVS-P			D200 J600	87-017-925-07(87-099-591-01(
	87-017-989-080	C-DIODE, F1J6	C609	87-010-831-08		U 0.1-16F			J900 L100	87-099-575-010 87-A50-009-080	JACK, 3.5ST BLK 5P		
			C610 C612	87-016-442-08 87-A10-047-08		S 33-10 OS SL ,U 1-10 Z F			L505	87-A50-108-08			
	87-047-156-010	BAT. PAS920L-VL3	C613 C614	87-A10-057-08 87-A10-019-08		TN 100-4 F95 E			L102	87-A50-012-08			
	87-012-286-080	C-CAP,U 0.01-25 B	C615	87-010-829-08		U 0.047-16F			L200 L201	87-005-778-08 87-005-777-08			
	87-A10-048-080 87-A10-048-080	C-CAP, TN 22-4 F95 S C-CAP, TN 22-4 F95 S	C616	87-012-274-08		U 1000P-50 B			L300 L500	87-A50-009-08 87-A50-013-08	0 C-COIL, 22UH NLC25		
	87-012-274-080	C-CAP,U 1000P-50 B	C617 C618	87-016-564-08 87-010-831-08		,10-6.3 PSN ,U 0.1-16F							J
	07 010-039-000	C-CAP.U 0.047-16F	C619	87-010-831-08	O C-CAP	U 0.1-16P			L501 L502	87-003-243-08 87-A50-012-08			
	87-012-272-080 87-012-284-080	-C-CAP.U 680P-50 B C-CAP.U 6800P-50 B	C623	87-A10-019-08		TN 10-6.3 A			L503 L505	87-005-778-08 87-005-778-08	O C-COIL, 10UK NLC25		
	87-A10-025-080 87-A10-047-080		0624 0625	87-312-286-38 37-312-286-38	0 0-CAP	UU 0.01-25 B UU 0.01-25 B			L600	87-005-779-08			BA
	40 1 409 08	1 (AP 15 L. 14 1 15F)	0626 2627	87-112-286-08 87-A11-035-08	0 0-CAP	.U 0.01-25 B TN 2.2 4 SVS-P							
	80 - A 871 - B	AF CONTROL OF	15.03 15.03	87 .11-274 - 3		10 10008-50 B							

87-A50-012-080 87-A50-010-080 C-COIL, 100UH LQH3C C-COIL, 22UH LQH3C C-COIL, S 10UH K 87-003-243-080 87-003-243-080 87-005-774-080 C-COIL. 4BLH 87-A50-013-080 C-COIL, 560UH LQH3C 87-A50-014-080 C-COIL, MCZ2525AT102 87-022-247-080 C-RES, U 22K-1/16W P 87-022-284-080 C-RES.U 68K-1/16W F 87-022-290-080 C-RES.U 220K-1/16W F 87-022-290-080 C-RES,U 220K-1/16W F 87-022-294-080 C-RES,U 470K-1/16WF C-RES,U 15K-1/16W F 87-022-243-080 87-022-248-080 C-RES,U 24K-1/16W F 87-036-269-080 SW, PUSH ESE102MH1 87-236-273-080 C-SW. TACT STEM 87-336-373-080 C-SW, SLIDE 1-1-2(E) 87-236-373-080 C-SW, SLIDE 1-1-2(E) 37-024-409-080 C-SFR, 22K RH03A3A 37-324-411-080 C-SFR, 47K RH03A3A 8--324-407-080 C-SFR, 10K RH03A3A 87-030-359-080 C-VIB.XTAL 45.158MHZ 37-030-358-080 C-VIB.XTAL 32.768KHZ C-VIB.CER PBRC12.00B 37-030-369-080 . В 87-A10-019-080 C-CAP, TN 10-6.3 A 87-A10-019-080 C-CAP. TN 22-4 F95 S C-CAP.S 820P-50 SL 87-010-177-080 C-CAP. TN 10-6.3 A 87-310-019-080 C-CAP,U 0.047-16F 87-010-829-080 87-016-296-080 C-CAP, TN 22-4 SV(A) 87-016-296-080 C-CAP, TN 22-4 SV(A) 87-016-296-080 C-CAP. TN 22-4 SV(A) C-CAP, TN 100-4 F95 E 87-A10-057-080 87-A10-057-080 C-CAP, TN 100-4 F95 E 87-A60-089-080 C-CONN, 18P CPP55 87-A60-093-080 C-CONN, 6P CFP55 87-A50-011-080 C-COIL, 47UH LQH3C 87-036-269-080 SW, PUSH ESE102MH1 87-036-312-080 C-SW. PUSH 102MH4 . В 87-010-787-080 C-CAP,U 0.022-25B 87-010-829-080 C-CAP,U 0.047-16F 87-010-831-080 C-CAP,U 0.1-16F 87-012-274-080 C-CAP,U 1000P-50 B 87-012-274-080 C-CAP,U 1000P-50 B 87-012-274-080 C-CAP,U 1000P-50 B 87-010-831-080 C-CAP,U 0.1-16F 87-012-280-080 C-CAP,U 3300P-50 B C-CAP, S 1-16 F 87-010-805-080 C-CONN, 11P CFP55 87-A60-092-080 FF-CABLE, 11P 0.5 69M 85-HM1-604-010 85-HM1-605-010 FF-CABLE, 4P 2 11MM 37-A50-012-080 C-COIL, 100UH LQH3C 37-036-312-080 C-SW, PUSH 102MH4 C-SW, PUSH SPPW9-4.8 87-036-350-080 C-SW, PUSH SPPW9-5.45 87-036-366-080 87-A90-109-080 C-SW, PUSH 4-1-1 SPPW 3~-A60-087-010 JACK, GP1F362R

PART NO.

カンリ NO.

DESCRIPTION

REE NO PART NO カンリ DESCRIPTION REF. NO PART NO. カンリ DESCRIPTION NO. FLEX TACT 3.8 EL800 85-HM1-602-110 EL,5HM-1 LM-G 85-HM1-606-010 PWB, FLEX TACT LCD800 85-HM1-609-010 LCD, 5HM-1 FLEX EL C.B FLEX BATT C.B C870 87-A10-126-080 C-CAP,U 100P-200J SL 85-HM1-603-010 C-TRANS, EL SU-202(4) ○ チップ抵抗部品コード/CHIP RESISTOR PART CODE チップ抵抗部品コードの成り立ち Chip Resistor Part Coding 桁表示 A 抵抗部品コード Figure Resistor Code 抵抗值 Value of resistor チップ抵抗 Chip resistor 容量 記号 寸法/Dimensions (mm) 抵抗コード : A 種類 許容誤差 Wattage Symbol 外形/Form LWt Resistor Code: A Type Tolerance 1/16W 1608 ±5% CJ 1.6 0.8 0.45 108 2 1.25 0.45 118 1/10W 2125 ±5% CJ CJ 3.2 1.6 0.55 128 1/8W 3216 ±5% TRANSISTOR ILLUSTRATION



2SA1213 2SB1571 2SD2402

f 1



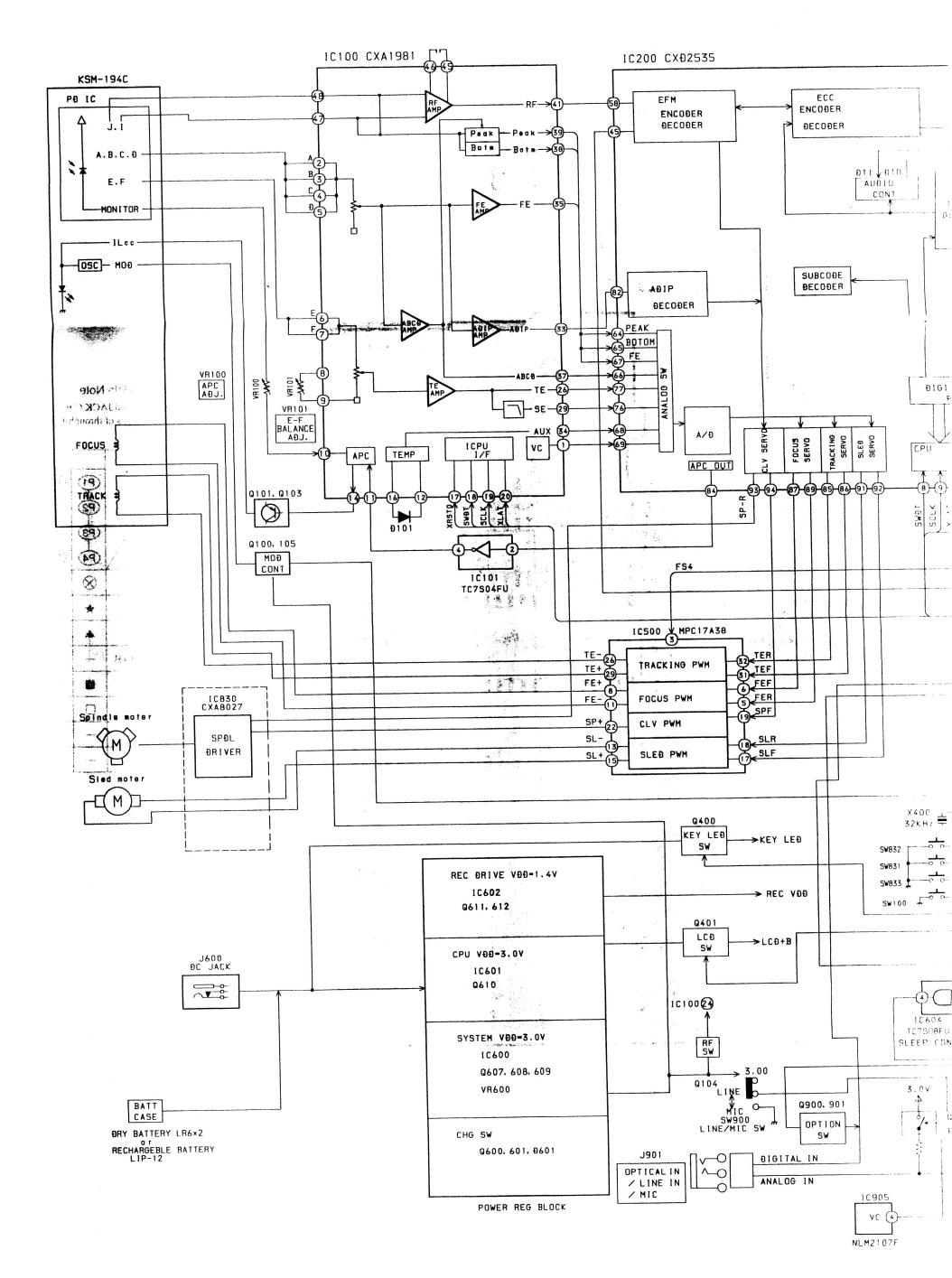
2SA1586 2SA1588 2SC4116 2SC4617 DTA123JE DTC123JE DTC123JE DTC144EE DTC323TU

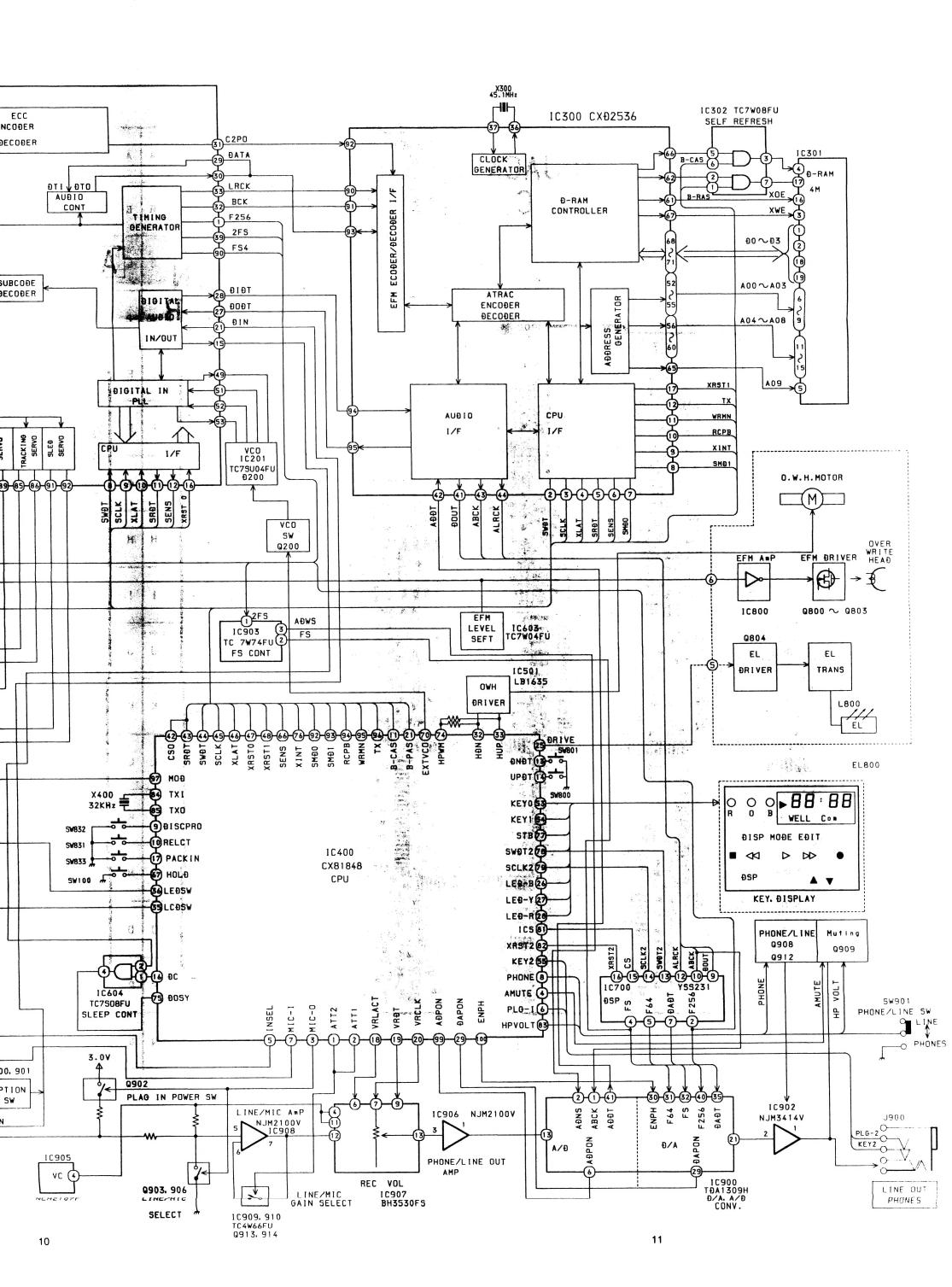


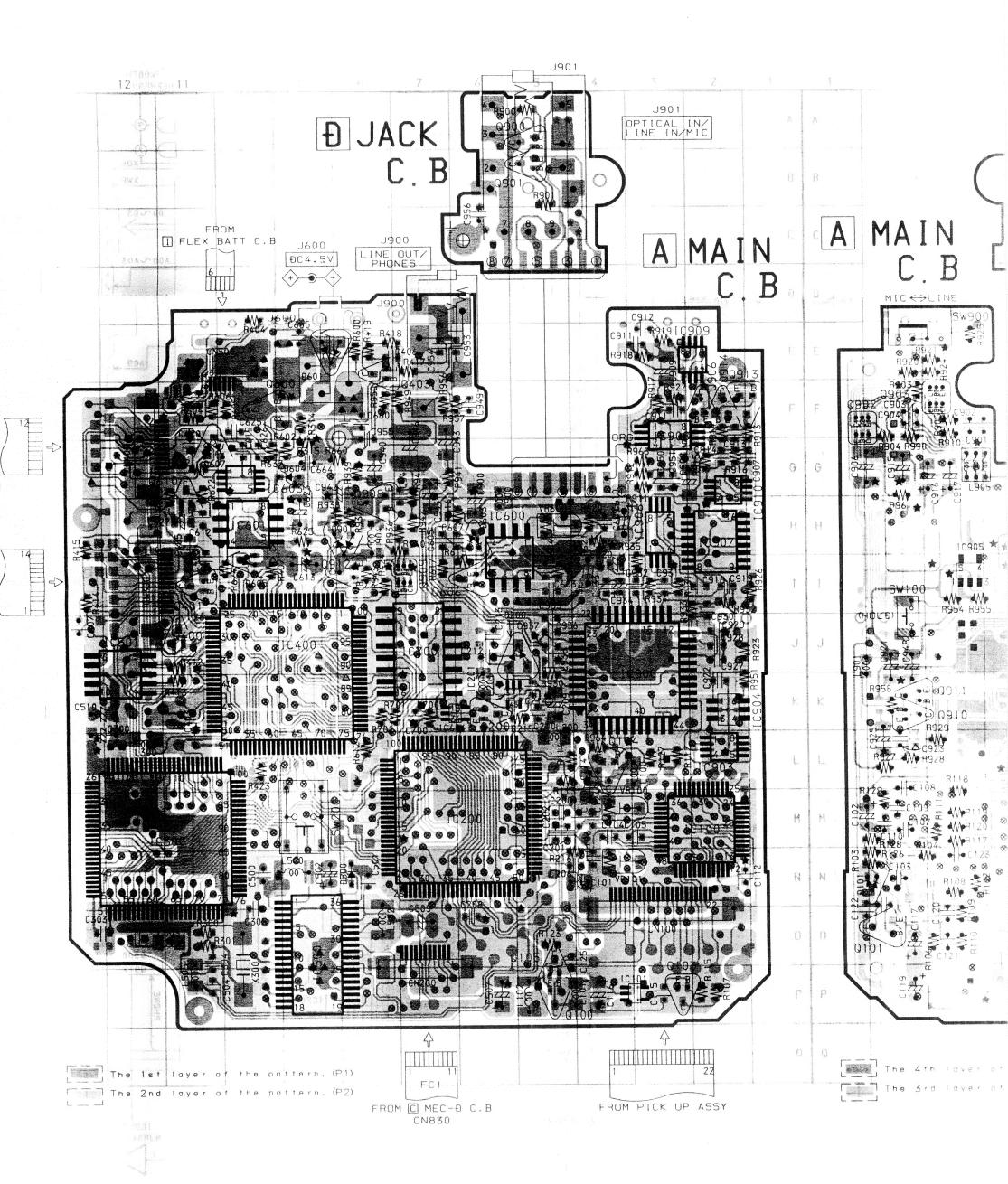
HA1A01FU RN1907 RN2911



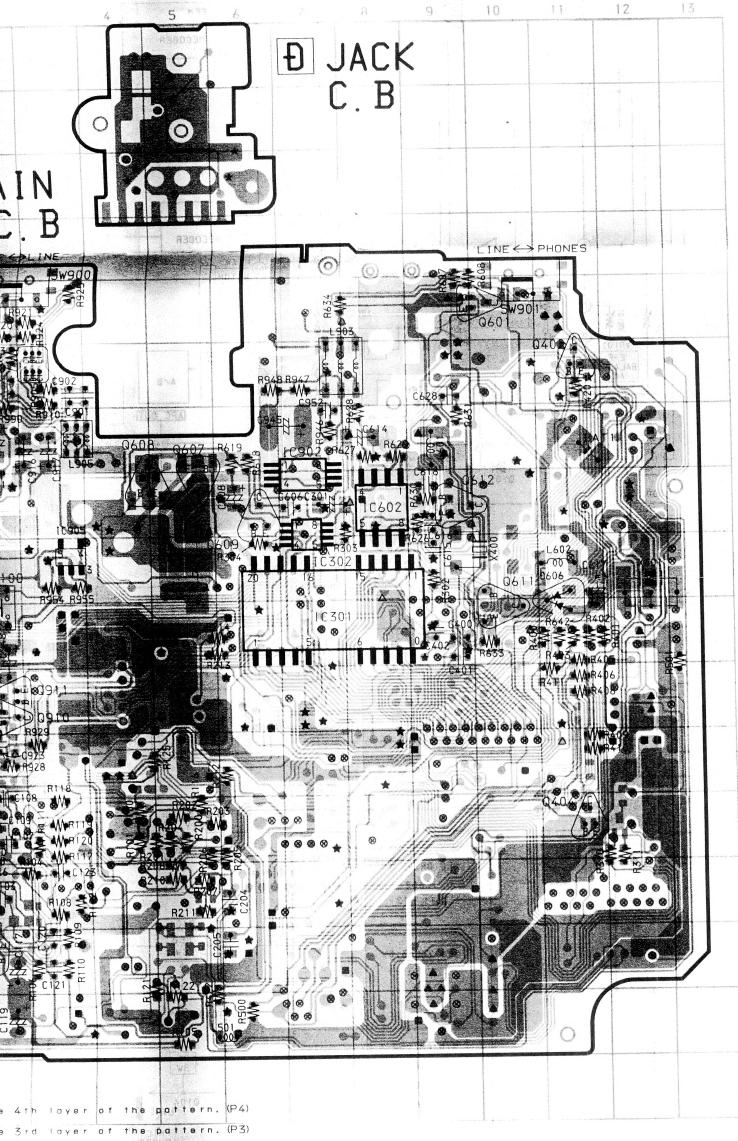
2SK1579 2SK2315







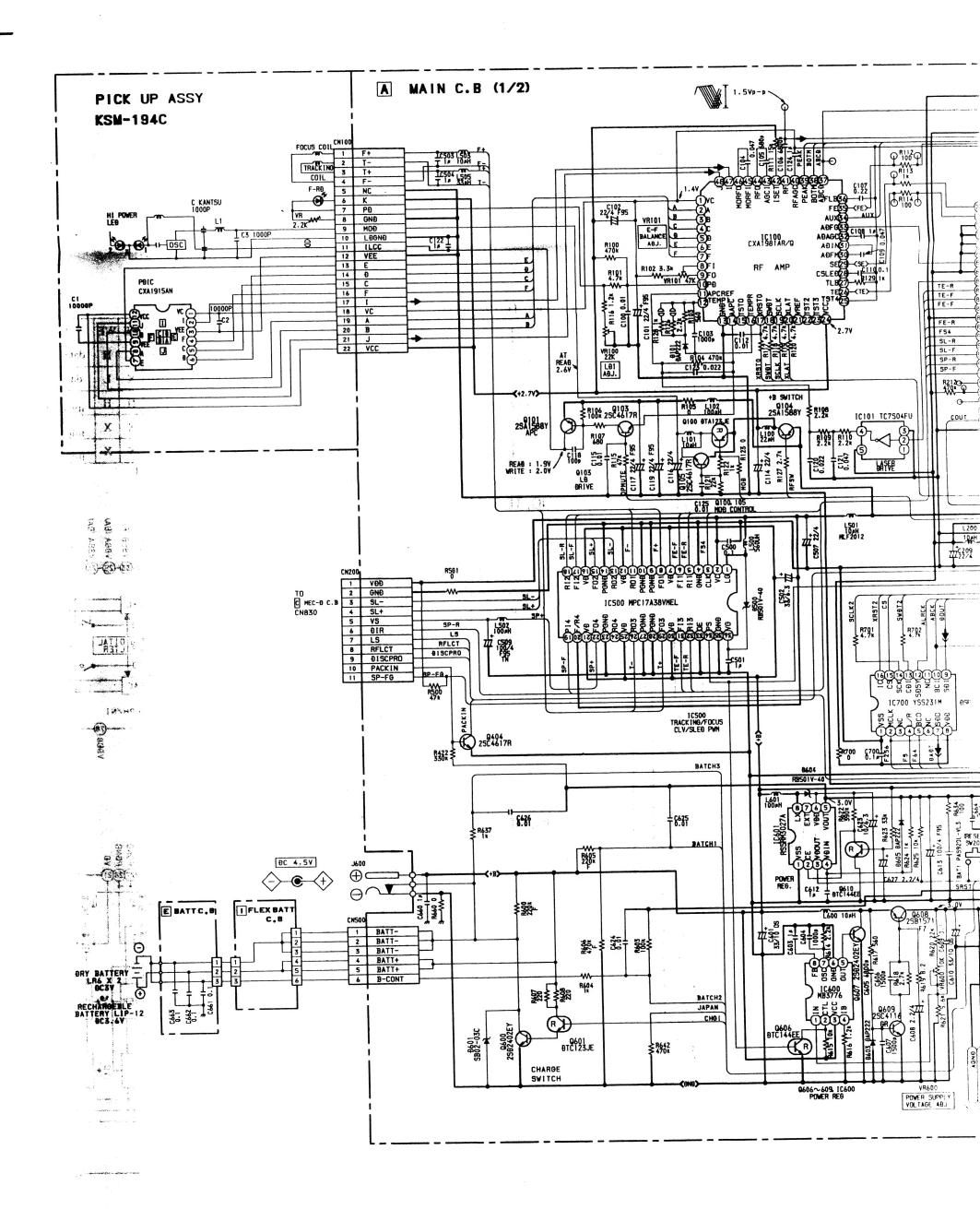


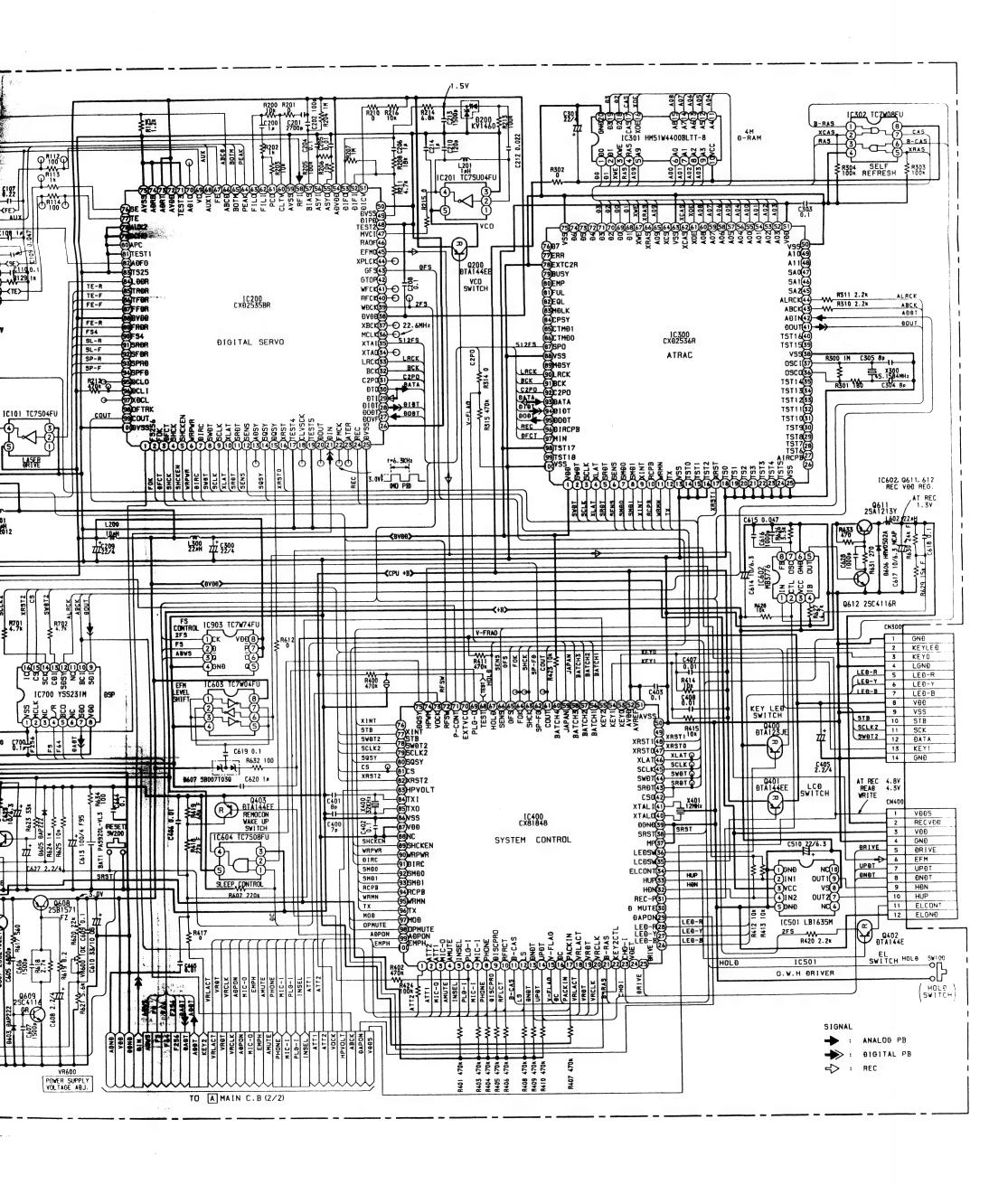


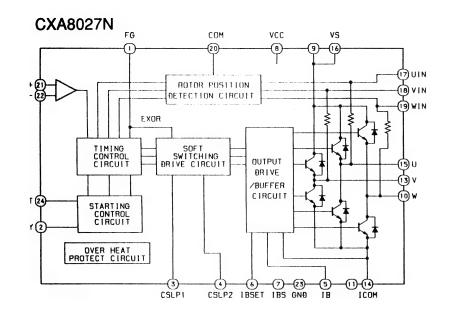
Through-Hole Note

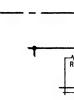
MAIN C.B and JACK C.B are the four-layer circuit boards. Different types of through-hole connection are identified as shown below.

		C	onducti	on Stat	е	
	P1)	P1)	(P1)	P1)	(P1)	
	P2		P2)		P2)	
	P3			P3		(P3)
	P4)	P4)	P4)	P4)		(P4)
SIGNAL	\otimes	\otimes	\otimes	\otimes	0	×,
GND	*	*	*	*	*	
+B	A	A	A	A		A
SYSCON +B		Δ	Δ	Δ		Δ
DVDD		\$4		81		
AVDD				44 44 74 74		
+2.7V			and other field	•		
+2.8V		()	0			

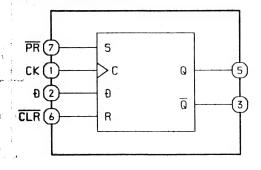








IC, TC7W74FU

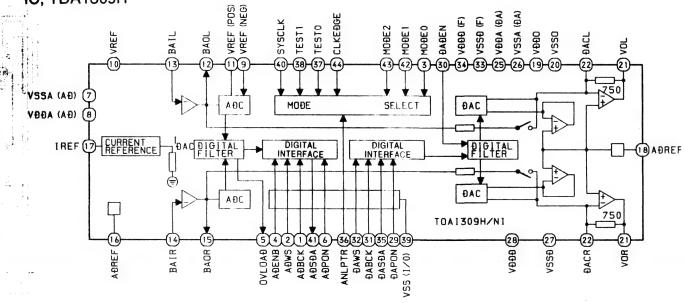


TRUTH TABLE

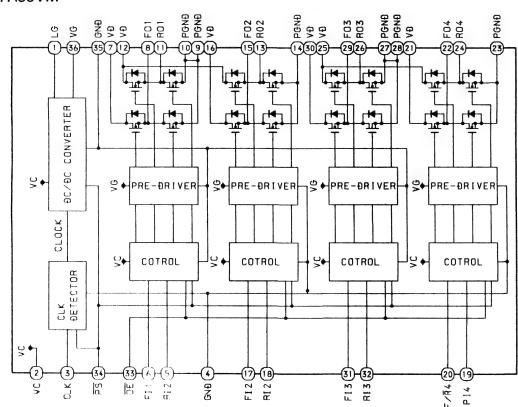
TROTH TABLE								
MODE	CEI	CE2	ŌE	WE	1/0	power source current		
read cycle	L	Н	L	Н	data output	ICCA		
write cycle	L	Н	х	L	data input	ICCA		
output disable	L	Н	Н	Н	hight impedance	ICCA		
no choice	Н	х	X	x	hight impedance	ICCS		
no choice	X	L	X	x	hight impedance	ICCS		

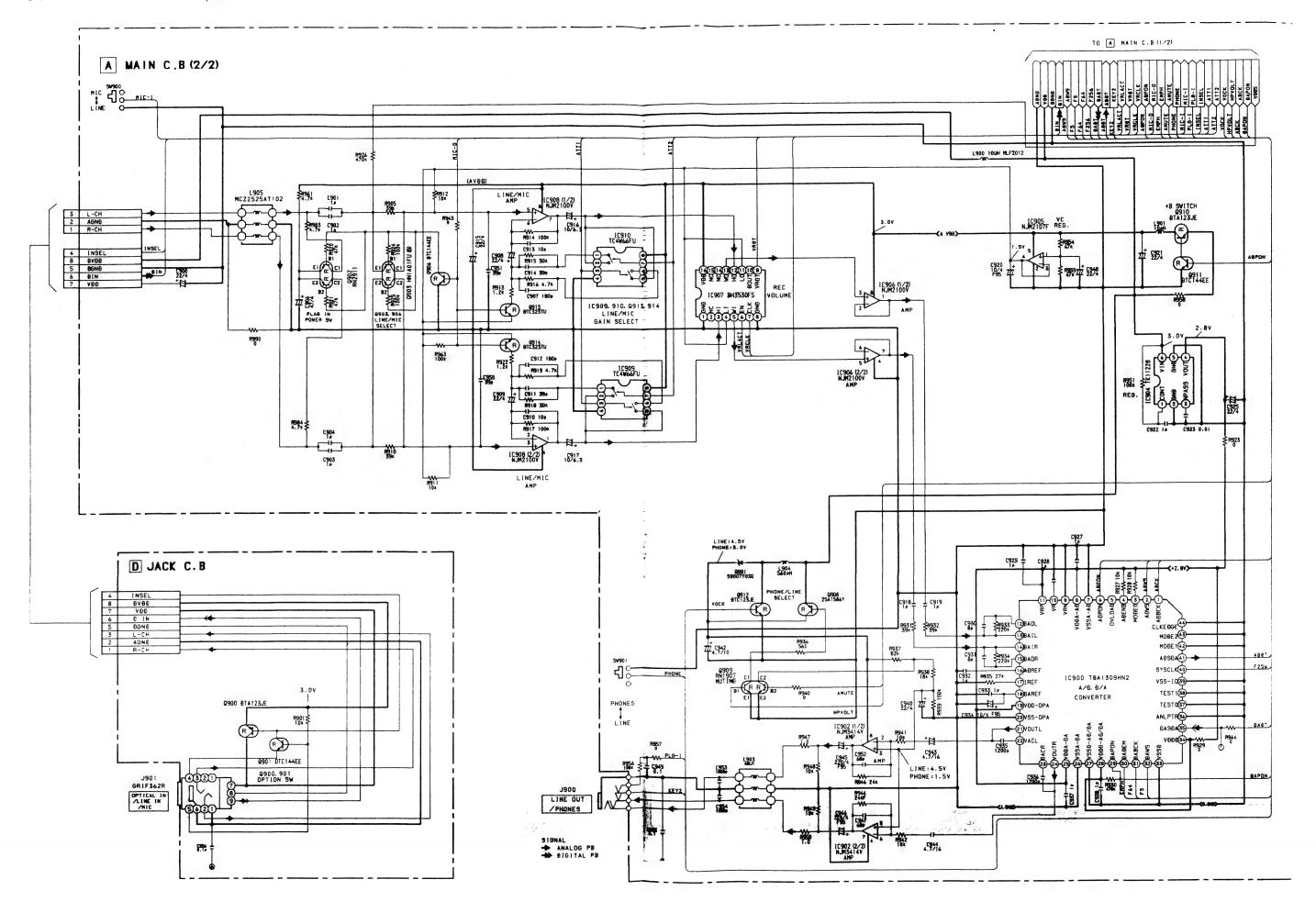
 $X: H ext{ or } L$

IC, TDA1309H



IC, MPC17A38VM



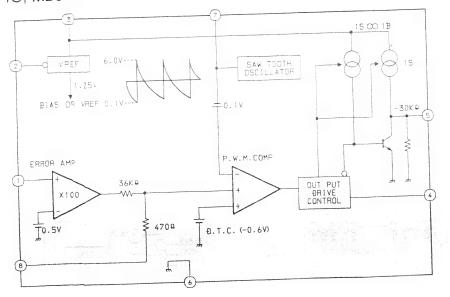


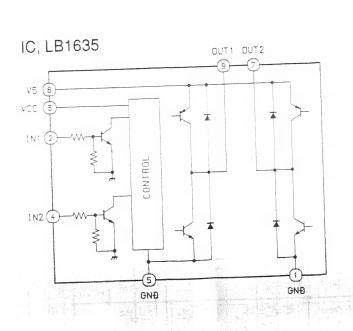
STAR

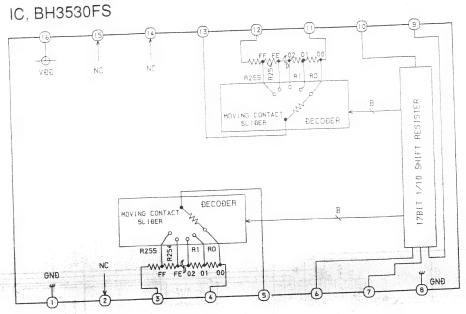
STB

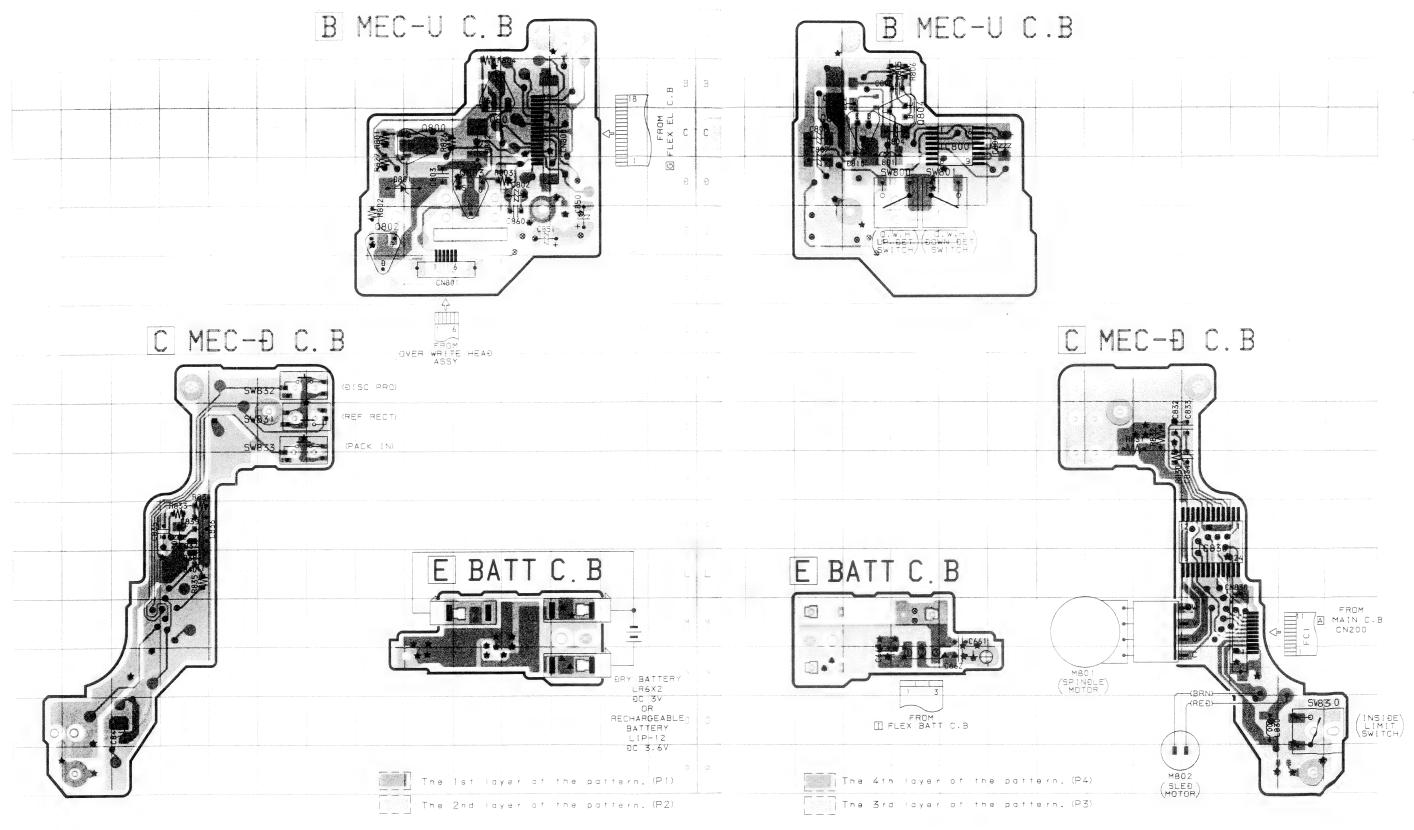
IC BLOCK DIAGRAM

IC, MB3776F



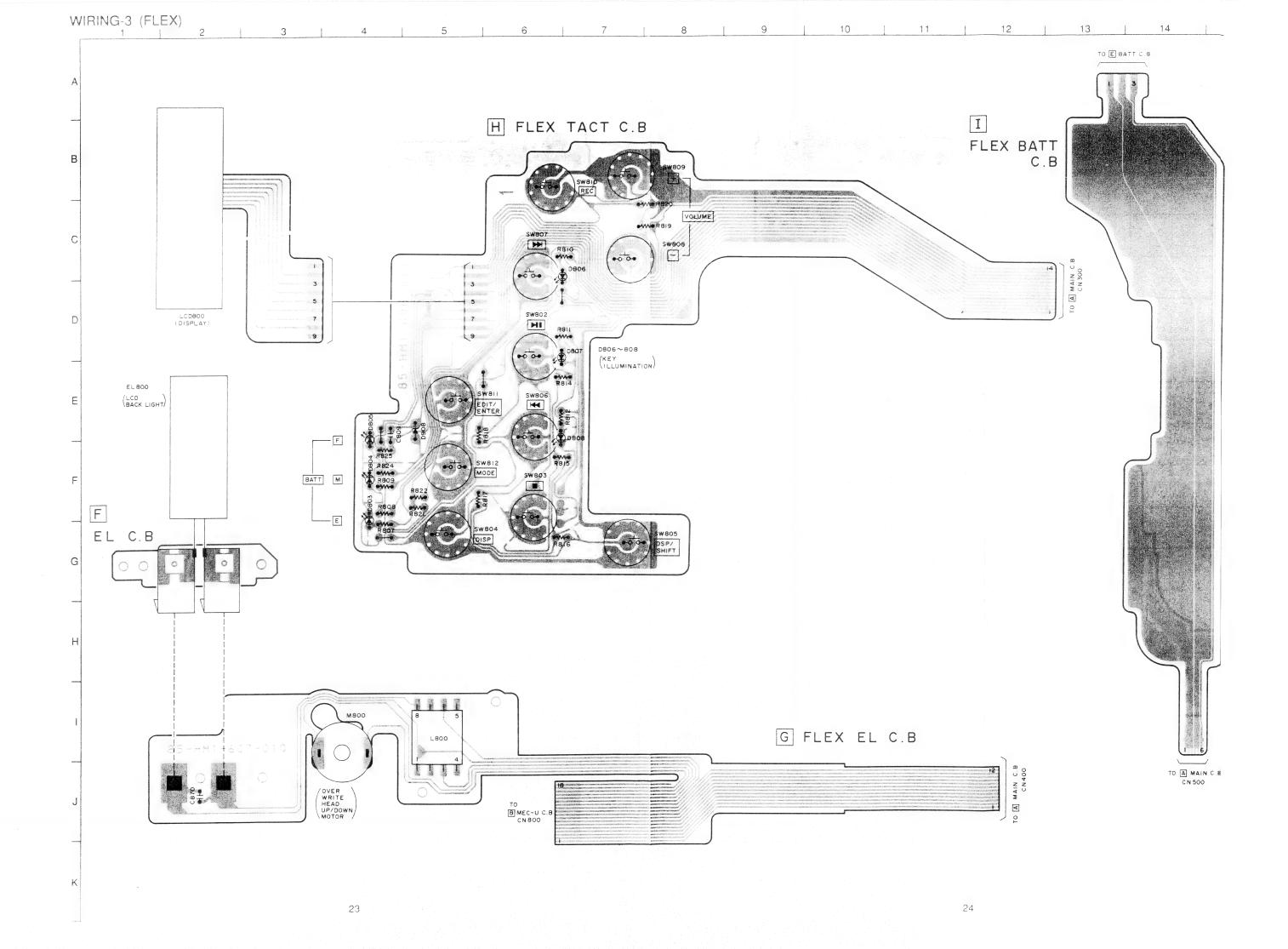


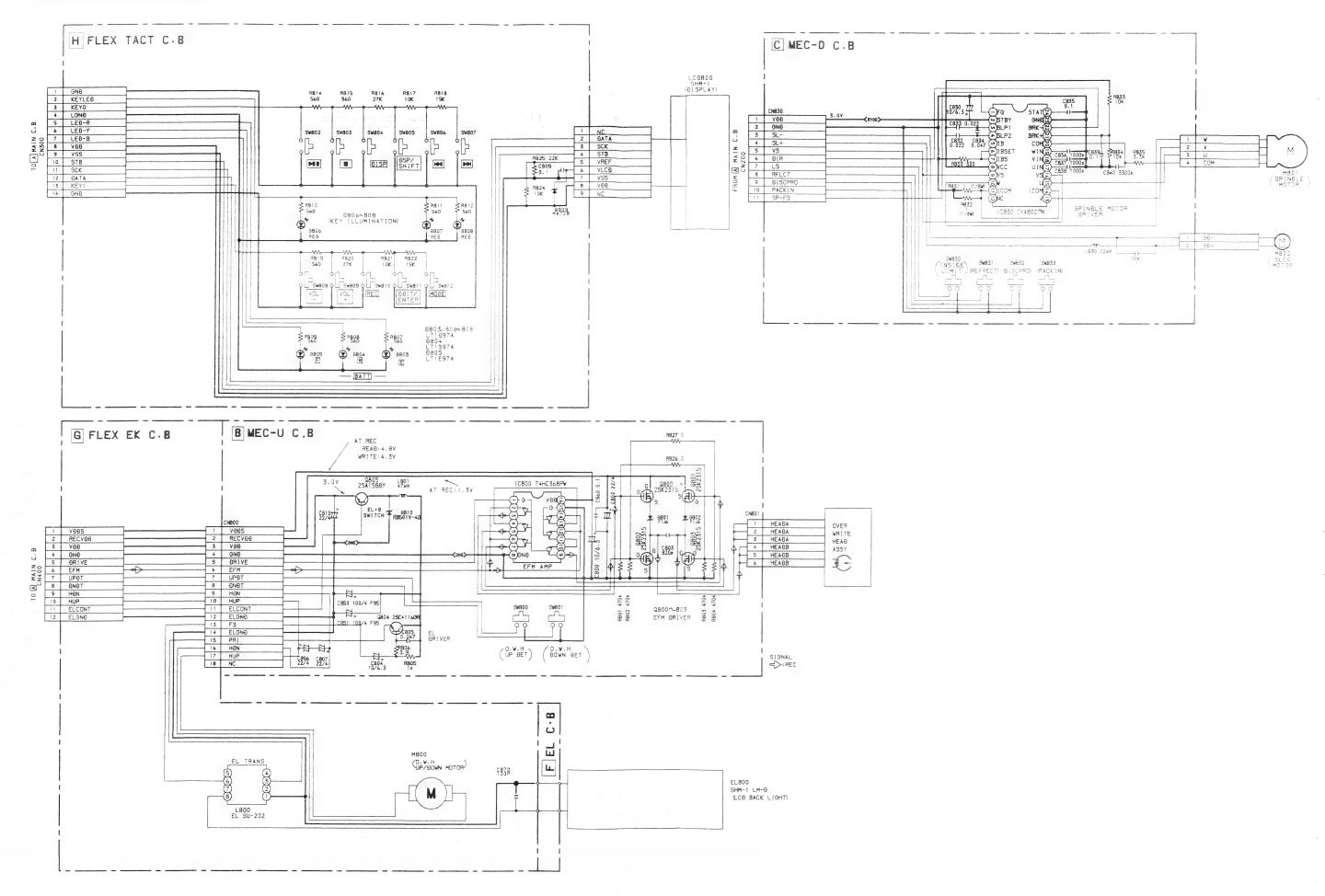




Through-Hole Note

REC C.B. SPDL C.B and BATT C.B are the four-layer circuit boards. Different types of through-hole connection are identified as shown below.





IC, CXD2535BR

Pin No.	Pin Name	1/0	Description
ı į	F\$256	0	256 Fs output. (11.2896 MHz)
2	FOK	0	Focus OK signal output. Focus OK at "H".
3	DFCT	0	Defect sense output. Defect at "H".
4	SHCK	0	Track jump sense output.
5	SHCKEN	I	Track jump sense enable input. Enable at "H".
	WRPWR	ı	Laser power switching input. Laser record power at "H".
6	WKFWK		Laser playback power at "L".
7	DIRC	1	Track jump control signal.
8	SWDT	I	Microprocessor serial interface data input.
9	SCLK	1	Microprocessor serial interface shift clock input.
10	XLAT	i	Microprocessor serial interface latch input. Latch at fall-down.
11	SRDT	0	Microprocessor serial interface data output.
12	SENS	0	Internal status is output corresponding to microprocessor serial interface address.
13	ADSY	0	ADIP sync output
14	SQSY	0	Disc sub-code Q sync/ADIP sync output.
	5001		U-bit CD or subcode Q sync output of the MD format is output when the source of the
15	DQSY	0	DIGITAL IN is CD or MD.
16	XRST	I	Reset input. Reset at "L".
17	TEST4	I	Test terminal. Connect to GND.
18	CLVSCK	0	Clock output for spindle servo evaluation. (5.6448 MHz).
19	TEST5	I	Test terminal. Connect to GND.
20	DOUT	0	Digital audio interface signal output.
21	DIN	I	Digital audio interface signal input.
22	FMCK	0	ADIP FM demodulation clock output.
23	ATER	0	ADIP CRC flag output. Error at "H".
24	REC	1	Switching between record and playback. Record at "H". Playback at "L".
25	DVSS	1	Digital GND.
26	DOVF	1	V-bit input of the signal to be output from the DOUT terminal.
			Signal to be output from the DOUT terminal and audio data input for peak level
27	DODT	I	detection.
28	DIDT	0	Audio data output of the signal which is input from the DIN terminal.
29	DTI	1	Record data input from CXD2536A.
30	DTO	0	Playback data output to CXD2536A during playback. "Z" during recording.
		+	C2 pointer of the playback data is output during playback.
31	C2PO	0	V-bit of the DIGITAL IN is output during digital recording. Analog recording at "L
32	BCK	0	64 Fs output. (2.8224 MHz).
33	LRCK	0	Fs output (44.1 kHz).
3.1	XTAO	0	Crystal oscillator circuit output terminal. (Inverted output of the X'TAL terminal).
35	IATX	1	Crystal oscillator circuit input terminal. (512 Fs = 22,5792 MHz).
36	MCLK	0	Master clock output. (512 Fs = 22.5792 MHz).
	YBCK	+ 0	BCK inverted output. (312 ts = 22.372 MHz).

Pin No.	Pin Name	1/0	Description
38	DVDD	1-1	Digital power supply.
39	WDCK	0	2 Fs output. (88.2 kHz).
40	RFCK	0	Read frame clock output. (Fs/6).
41	WFCK	0	Write frame clock output.
42	GTOP	0	Monitoring the operating status of the frame sync protection window. Frame sync protection window is open at "H".
43	GFS	0	Frame sync OK at "H".
44	XPLCK	0	PLL clock output of the EFM decoder. (98 Fs = 4.3218 MHz).
45	EFMO	0	"L" output during playback. EFM encode data) output during recording.
46	RAOF	0	RAM overflow output during playback.
47	MVCI	ı	Clock input of the externally connected VCO for DIGITAL IN PLL.
48	TEST2	I	Test terminal. Connect to GND.
49	DIPD	0	DIGITAL IN PLL phase comparator output.
50	DVSS	1-	Digital GND.
51	DICV	ī	Voltage input to control the internal VCO for DIGITAL IN PLL.
52	DIFI	I	Input to filter when the internal VCO for DIGITAL IN PLL is used.
53	DIFO	0	Filter input when the internal VCO for DIGITAL IN PLL is used.
54	AVDD	_	Analog GND.
55	ASYO	0	Playback EFM full swing output. ("L" = Vss, "H" = VDD).
56	ASYI	1	Playback EFM comparator slice voltage input.
57	BIAS	I	Playback comparator bias current input.
58	RFI	ı	Playback EFM RF signal input/.
59	AVSS	T_	Analog GND.
60	CLTV	1	Master PLL for playback digital PLL and also input control voltage to internal VCO for recording EFM PLL.
61	PCO	0	Master PLL for playback digital PLL and also phase comparator output to recording EFM PLL.
62	FILI	I	Master PLL for playback digital PLL and also filter input to recording EFM PLL.
63	FILO	0	Master PLL for playback digital PLL and also filter output to recording EFM PLL.
64	PEAK	ı	Peak-hold signal input of laser light.
65	вотм	I	Bottom-hold signal input of laser light.
66	ABCD	ı	Laser light amount input signal.
67	FE	ı	Focus error signal input.
68	AUXI	I	Auxiliary input-1.
69	VC	ı	Center voltage input.
70	ADIO	0	Monitor output of the A/D converter input signal.
71	TEST3	I	Test terminal. Connect to GND.
72	AVDD	1-	Analog GND.
73	ADRT	ı	Input voltage indicating upper limit of A/D converter operation range.
74	ADRB	1	Input voltage indicating lower limit of A/D converter operation range.
75	AVSS	4.400	Analog GND.

IC, CXD2536R

Pin No.	Pin Name	1/0	Description
1	VDD	-	Power supply terminal.
2	SWDT	I	Microprocessor serial interface, data input.
3	SCLK	I	Microprocessor serial interface, shift clock input.
4	XLAT	1	Microprocessor serial interface, latch input. Latched at fall down.
5	SRDT	0	Microprocessor serial interface, data output.
6	SENS	0	Internal status output corresponding to the address of microprocessor serial interface
7, 8	SMD0, SMD1	I	Serial command, command mode.
9	XINT	0	Interrupt request output terminal. "L" when interrupt status occurs.
10	RCPB	I	"H" at record mode, "L" at playback mode.
11	WRMN	I	"H" at write mode, "L" at monitor mode.
12	TX	ı	Enable signal input terminal of the recording data output. Enable at "H".
13	VSS	_	GND.
14~16	TST0~TST2	I	Test terminal. Connect this pin to GND.
17	XRST	I	Reset input. Reset at "L".
18~21	TS0~TS3	I	Total Companies Collins
22~24	TST3~TST5	I	Test terminal. Connect this pin to GND.
25	VSS	-	GND.
			ATRAC block record/playback mode output.
26 AIRCPB		0	Record mode at "H". Playback mode at "L".
27~35	TST6~TST14	0	Test terminal. Leave this pin to open.
36	osco	0	Crystal oscillator circuit output terminal. (Inverted output of the OSC1 terminal).
37	OSCI	I	Crystal oscillator circuit input terminal. (1024 Fs).
38	VSS	_	GND.
39, 40	TST15, TST16	0	Test terminal. Leave this pin to open.
41	DOUT	0	Record monitor output/decoded audio data output.
42	ADIN	I	Analog record input terminal. (External A/D converter output is connected to this terminal.)
43	ABCK	0	XBCK (64 Fs) output terminal to external audio block.
44	ALRCK	0	LRCK (Fs) output terminal to external audio block.
45~47	SA2~SA0	0	SRAM address bus.
48, 49	A11, A10	0	RAM address bus.
50	VSS	_	GND.
51	VDD	_	Power supply terminal.
52~55	A03~A00	0	RAM address bus.
56~60	A04~A08	0	Mairi audices ous.
61	XOE	0	RAM output enable.
62	XCAS	0	RAM CAS output.
63	VSS	٥	GND.
64	xcs	0	RAM chip select. DRAM at "H". SRAM at "L".
65	A09	0	RAM address bus.
66	XRAS	0	RAM RAS output.

Pin No.	Pin Name	I/O	Description
67	XWE	0	RAM write enable.
68, 69	D1, D0	1/0	RAM data bus.
70~74	D2~D6	I/O	KANI USIS
75	vss	_	GND.
76	D7	ľO	RAM data bus.
77	ERR	ľO	Data input/output terminal for C2PO exclusive RAM.
78	EXTC2R	I	Selector of C2PO exclusive RAM. "H" to use. "L" not to use.
79	BUSY	0	Busy output of RAM access. RAM access at "H".
80	ЕМР	0	The signal output indicating immediately before empty or full of the ATRAC data.
81	FUL	0	The signal output indicating immediately before full or empty of the ATRAC data.
82	EQL	0	ATRAC data empty. ("H" when ASC = DSC).
83	MDLK	0	Indicating main/sub of the record/playback data. Sub or linking at "H". Main at "L".
84	CPSY	0	Sync input to be inserted internally.
85, 86	CTMD1, CTMD0	0	Internal counter mode output.
87	SPO	0	512 Fs output.
88	VSS	_	GND.
89	MDSY	0	Sync detection output of the main data.
90	LRCK	I	LRCK (Fs) input terminal from the EFM encoder/decoder.
91	BCK	I	BCK (46 Fs) input terminal from the EFM encoder/decoder.
92	C2PO	I	C2PO input terminal from the EFM encoder/decoder.
93	DATA	I/O	Data input/output terminal from the EFM encoder/decoder.
94	DIDT	I	Digital recording input terminal.
95	DODT	0	Record monitor output/decode audio data output.
06	DIRGER		Record/playback mode output to the EFM encoder/decoder.
96	DIRCPB	0	Record mode at "H". Playback mode at "L".
97	MIN	I	External monitor signal input terminal.
98	TST17	ı	Test terminal. Connect this pin to VDD.
99	TST18	0	Test terminal. Leave this pin to open.
100	VSS	0	GND.

C, CXA1981AQ/AR

1 *

Pin No.	Pin Name	1/0	Description		
l	VC	0	Vcc/2 voltage output.		
2	A	I	Main beam servo signal A, current input.		
3	В	I	Main beam servo signal B, current input.		
4	С	I	Main beam servo signal C, current input.		
5	D	ı	Main beam servo signal D, current input.		
6	E	1	Main beam servo signal E, current input.		
7	F	I	Main beam servo signal F, current input.		
8	FI	I	EF balance adjustment.		
9	FO	0	EF balance adjustment.		
10	PD	i	Amount of light, monitoring signal input.		
11	APCREF	ı	Reference voltage input for laser power setting.		
12	TEMPI	1	External temperature sensor is connected to this terminal.		
13	GND	—	GND.		
14	AAPC	0	APC output.		
15	TST0	0	Test terminal. Open.		
16	TEMPR	0	External temperature sensor is connected to this terminal. Reference voltage is out		
17	TST1	1	Test terminal. Connected to Vcc.		
18	SWDT	ı	Microprocessor serial interface, data input.		
19	SCLK	ı	Microprocessor serial interface, shift clock input.		
20	XLAT	ı	Microprocessor serial interface, latch input. Latch at "L".		
21	VREF	0	Reference voltage output.		
22	TST2	0	Test terminal. Open.		
23	TST3	+=	Test terminal. Open.		
24	VCC	+-	Power supply terminal.		
25	TST4	I	Test terminal. Connected to Vcc.		
26	TE	0	Tracking error output.		
20		+	External capacitor for low-boosting the tracking error signal, is connected to this		
27	TLB	-	terminal.		
		+-	External capacitor of low-pass capacitor for the sled error signal, is connected to the		
28	CSLED	_	terminal.		
29	SE	0	Sled error signal output.		
30	ADFM	0	ADIP FM signal output.		
31	ADIN	1	ADIP signal comparator input.		
32	ADAGC		External capacitor for ADIP AGC is connected to this terminal.		
33	ADFG	0	ADIP 2 binary signal output.		
34	AUX	0	13 output/temperature signal output. The two signals are selected by the serial		
			command.		
35	FE	0	Focus error signal output.		
10	(CO	\ -	Enternal vapavitor for low-boosting the focus error signal, is connected to this term		
37	ABCD	0	Light amount signal output from the main beam servo detector.		

32

Pin No.	Pin Name	I/O	Description		
76	SE	1	Sled error signal input.		
77	TE	i	Tracking error signal input.		
78	AUX2	1	Auxiliary input-2.		
			Connect to GND.		
79	DCHG	I			
80	TEST6	1	Test terminal. Connect to GND.		
81	TEST	I	Test terminal. Connect to GND.		
82	ADFG	ı	ADIP binary FM signal (22.05 ±1 kHz) input.		
83	TS25	I	Test terminal. Connect to GND.		
84	LDDR	0	Laser drive output.		
85	TRDR	0	Tracking servo drive output (-).		
86	TFDR	0	Tracking servo drive output (+).		
87	FFDR	0	Focus servo drive output (+).		
88	DVDD	_	Digital power supply.		
89	FRDR	0	Focus servo drive output (-).		
90	FS4	0	4 Fs output. (1764 kHz).		
91	SRDR	0	Slid servo drive output (-).		
92	SFDR	0	Slid servo drive output (+).		
93	SPRD	0	Spindle servo drive output (-).		
94	SPFD	0	Spindle servo drive output (+).		
95	DCLO	0	Serial data output for spindle servo evaluation.		
96	DCLI	I	Serial data input for spindle servo evaluation.		
97	XDCL	0	Serial data load signal output for spindle servo evaluation.		
98	OFTRK	0	OFF-track signal output. OFF-track at "H".		
99	COUT	0	Track jump count signal output.		
100	DVSS	_	Digital GND.		

Pin No.	Pin Name	I/O	Description			
39	PEAK	0	RF/ABCD peak hold signal output.			
40	RFAGC	1-1	External capacitor for RF AGC is connected to this terminal.			
41	RF	0	RF equalizer output.			
42	ISET	_	BPF (fo = 702 kHz, 22 kHz) and RF equalizer setting.			
43	AGCI	I	RF AGC input.			
44	RFO	0	RF amplifier output. Eye pattern check point.			
45	MORFI	I	The input signal in which the RF signal from groove and the RF signal are mixed.			
46	MORFO	0	RF signal (I-J) output from groove.			
47	I	I	I-V converted RF signal I input.			
48	J	ı	I-V converted RF signal J input.			

IC, YSS231

Pin No.	Pin Name	I/O	Description
1	VSS	_	GND.
2	MCLK	I	Clock input.
3	(NC)	_	
4	L/R	0	Digital audio output word clock.
5	BCO	0	Digital audio output bit clock.
6	(NC)	_	
7	SDO	0	Digital audio output serial data.
8	VDD	_	Power supply terminal.
9	SDI	I	Digital audio input serial data.
10	BCI	I	Digital audio input bit clock.
11	(NC)	_	
12	SDSY	I	Digital audio input word clock.
13	CDI	I	Microprocessor interface serial data.
14	SCK	I	Microprocessor interface serial clock.
15	CS	I	Microprocessor interface chip select.
16	IC	I	Initial clear input.

IC, CX81848

Pin No.	Pin Name	1/0	Description				
1	ATT2	0	1 1 0 Switching between				
2	ATTI	0	1 0 0 MIC amp or Gain				
3	MIC-O	0	MIC/LINE identification output. H: MIC.				
4	AMUTE	0	Analog mute. H: MUTE.				
5	INSEL	I	Analog/digital input select. H: Analog.				
6	PLG-I	ı	Presence of HP plug. L: Plug is present.				
7	MIC-I	I	MIC/LINE select switch judgment. H: MIC.				
8	PHONE	I	LINE/PHONE select switch judgment. H: LINE.				
9	DISCPRO	I	Disc recording enabled switch judgment. L. Enabled.				
10	RFRCT	1	Disc reflection factor sensor switch judgment. L: Low reflection.				
11	B-CAS	0	DRAM self-refresh control.				
12	LS	1	The sled's innermost circumference detection.				
13	DNDT	I	Over-write head down detection.				
14	UPDT	1	Over-write head up detection.				
15	V-FLAG	I	Subcode V flag input included in the digital IN signal.				
16	DC	I	Not used.				
17	PACKIN	I	Jacket presence sensor switch judgment. H: Jacket present.				
18	VRLACT	0	Record VOL control LACT.				
19	VRDT	0	Record VOL control DATA.				
20	VRCLK	0	Record VOL control CLK.				
21	B-RAS	0	DRAM self-refresh control.				
22	KEY2CTL	0	Remote control (Key2) Wake up control. "L" during sleep.				
23	CHG-I	0	Charge control. "H" during charging.				
24	VDET	0	Not used.				
25	DRIVE	0	Record current control. "L" during recording.				
26	LED-B	0	LED indicating battery's remaining power signal. (blue).				
27	LED-Y	0	LED indicating battery's remaining power signal. (yellow).				
28	LED-R	0	LED indicating battery's remaining power signal. (red).				
29	DAPON	0	D/A converter ON/OFF control. "L": OFF. Open drain.				
30	DMUTE	0	Not used. Open drain.				
31	REC-P	0	Recording circuit power supply ON/OFF control. L: ON. Open drain.				
32	HDN	0	Over-write head DOWN control. OPEN: head DOWN. Open drain.				
33	HUP	0	Over-write head UP control. OPEN: head UP. Open drain.				
34	ELCONT	0	EL ON/OFF control. L: ON. Open drain.				
35	LCDSW	0	LCD ON/OFF control. L: ON. Open drain.				
36	LEDSW	0	Key light ON/OFF control. L: ON. Open drain.				
37	MP	I	Connected GND.				
38	SRST	ı	System reset. L: reset.				
39	DGND	_	Digital GND.				
40	XTALO	0	Inverter oscillating system clock of this microprocessor. f = 12 MHz.				
41	XTALI	1	inverter oscinating system clock of this microprocessor. 1 = 12 MHZ.				

Pin No.	Pin Name	1/0	Description
42	CSO	1	Connected to 43 pin SRDT.
43	SRDT	I	Serial data input.
44	SWDT	0	Serial data output.
45	SCLK	0	Serial clock output.
46	XLAT	0	Serial data strobe output.
47	XRST0	0	CXA1981. CXD2535 reset control. L: reset.
48	XRSTI	0	CXD2536 reset control. L: reset.
49	NC	0	Not used.
50	AVSS		A/D converter GND.
51	AVREF		A/D converter upper limit set.
52	AVDD	1_	A/D converter VDD.
53	KEY0	1	PLAY, STOP, DISP, DSP, B-SKIP, F-SKIP key judgment inputs.
54	KEY1	ı	VOL DOWN, VOL UP, REC, EDIT, MODE key judgment inputs.
55	KEY2	1	Remote control key judgment inputs.
56	BATCHI	I	Battery's remaining power detected signal input.
57	BATCH2	I	Battery type judgment input.
58	ВАТСН3	i	Input signal indicating remaining power to be charged.
59	JAPAN	I	Japan or overseas specification judgment input. H: Japan.
60	BATCH4	ī	System power check input.
61	COUT	I	Track jump count input.
62	SP-FG	ı	Spindle FG input.
63	SHCK	I	Shock detection input.
64	FOK	I	FOCUS-ON input. H: FOCUS-ON.
65	GFS	I	Guard frame sync input. H Frame sync OK.
66	SENS	I	CXD2535 internal status monitor input.
67	HOLD	I	HOLD key input. L: HOLD.
68	TEST	I	Test mode identification. After reset, L: TEST mode.
69	PLG-O	0	PLG-I pull-up output to prevent remote control from misoperation at pin-6.
70	EXTVCO	0	VCO ON/OFF control. L: ON.
71	P-CONT	0	System power supply ON/OF control. H: ON.
72	RFSW	0	RF system power supply ON/OF control. L: ON.
73	VOCK	0	Voltage control when audio output is "LINE". PWM output.
74	HPWM	0	PWM control during head UP/DOWN.
75	DQSY	I	SUB-Q sync input during digital recording.
76	XINT	1	CXD2536 interrupt request input. L: interrupt request.
77	STB	0	LCD serial data strobe output.
78	SWDT2	0	DSP, LCD serial data output.
70	SCLK2	-	DSP, LCD serial clock output.
80	YZQZ	1	EFM subcode Q sync and ADIP sync input.
81	CS	0	DSP serial data strobe output.
82	XRST2	0	DSP reset output. L: reset.

Pin No.	Pin Name	I/O	Description			
83	HP VOLT	0	Voltage control when audio output is "PHONE". "H" during PHONE.			
84	TXI	I	Inverter for oscillating clock for watch and during sleep. f = 32.768 kHz			
85	тхо	O	attach			
86	VSS	- a/— &	GND.			
87	VDD		VDD.			
88	NC		Connected VDD.			
89	SHCKEN	0	Shock detection request output.			
90	WRPWR	0	Laser power control to CXD2535. H: laser power.			
91	DIRC	0	Track jump control.			
92	SMD0	0	CXD2536 serial data control mode.			
93	SMD1	0	CXD2536 serial data control mode.			
94	RCPB	0	CXD2536 record/playback control. H: record.			
95	WRMN	0	ATRAC encode and external RAM write control. H: acknowledged.			
96	TX	0	CXD2536 record data output enable control. H: acknowledged.			
97	MOD	0	Light-emitting laser diode RF super-imposing control.			
98	OPMUTE	0	Light-emitting laser diode ON/OFF control. H: ON.			
99	ADPON	0	A/D converter ON/OFF control. H: ON.			
100	ЕМРН	0	Emphasis ON/OFF control. H: ON.			

The AM-F3 has the built-in TEST MODE to be used for adjustment and operation check.

1. How to Activate and Cancel the TEST MODE, and Caution of usage.

(1) How to Active the TEST MODE.

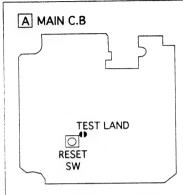
- 1) Short-circuit the TEST LAND (soldering lands) using solder.
- 2) Turn on the main power and press the RESET SW. Check that all LCD's and LED's turn on the following characters and displayed sequentially. "Welcome to Mini Disk World".

(2) How to Cancel the TEST MODE.

1) Turn off the main power. Remove soldering form the TEST LAND.

(3) Caution

- 1) The TEST MODE takes place ignoring all mechanical abnormal actions. Therefore, if a machine should show any abnormal actions, remove the main power immediately.
- 2) Music data cannot be recorded nor played back during the TEST MODE.
- 3) When down condition the OWH (Over Write Head), can not eject. As EJECT lever is lock.
- 4) While TEST MODE and EJECT condition (disk holder is open). OWH can move up and down. Whoever should care disk (during test mode) when OWH down position. If so OWH get damage.



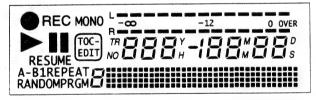


fig-2 All segments of LCD is illuminated

2. The Items Which Can Be Checked While Activating the TEST MODE.

(1) Displays

All LCD's and LED's turn on.

(2) Audio Output.

The audio PB circuits (DAC, LINE amplifier and HP amplifier) can be checked. LINE amplifiers output and HP amplifiers output as Follows:

* 1kHz sine wave is -12dB is output from LINE OUT.

When press any operation key, then stop audio output. How to return audio output is press the "RESET" key and start condition at TEST MODE.

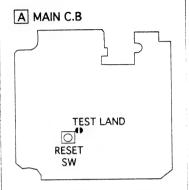


fig-1 Position of TEST LAND and RESET swith

5	How to	Check M	ovement	of the	OWH	Over-Write	Head
Э.	HOW IO	CHECK	iovement	or trie	CAAL	Over-write	neau

If VOL key is pressed while the disk drive is in STOP mode, OWH movement can be checked. If "+" key is pressed, the OWH starts moving up. If "-" key is pressed, the OWH starts moving down.

* Thereafter items change mode at stand by condition of servo. If a set is the first condition of TEST MODE (all light LCD display), press the "STOP" key, become stand by condition of servo.

The audio recording circuit (MIC amplifier, line amplifier and ADC = AD converter) can be

(Input the digital audio source or analog audio source from external source in STOP mode. Press

The Following switches (a set and inside of a mechanism) can be checked as follows by

"ON" when DISC is IN

(LIMIT SW is ON)

HOLD SW is "ON"

Switch Status

"ON" when WRITE PROTECT tab is clossed (record enable) of a

"ON" when a disk is high reflection (CD) type

"ON" when Pick-up position is the most inside

"ON" when OWH (Over Write Head) moved UP

"ON" when OWH (Over Write Head) moved DPWN

"ON" when oputical cable is connected (But LCD light is OFF)

LCD Display

REPEAT

TR

NO

MONO

TOC-

EDIT

RESUME

A

-- ∞

3. How to check the Audio Recording using the Record Monitor Function.

REC key. The RECORD monitor state is established in accordance.

DISC

4. How to Check the Switches

monitoring the LCD

Switch Name

REC PROTECT

REFLECT

DISC IN

INNER

UP DET

DOWN DET

HOLD INPUT SEL

"Caution"

- * Do not try to move down the OWH while a CD disk is being inserted.
- * With care insert disk when OWH down position. If so OWH get damage.

6. How to Check the SLED Motion.

Sled motor and Pick-up movement can be check by key and key. (key : move to out side. key : move to in side.)

7. How to Check the Laser Power

While the MD disk drive is in STOP mode, press EDIT/ENTER key. Every pressing of the EDIT/ENTER key advances in the order : OFF → CD-R → MO-R → 1/2 LASER → MO-W → OFF and repeat this order. The laser power status appears also on the LCD level meter. It does not measure and show the actual laser power emitted. It shows the internal operation mode.

This TEST MODE can be able to use Laser power adjustment.

See passes 44, 45)

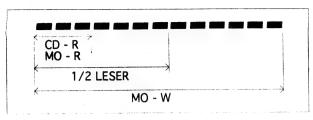


fig-3 Level meter display of LCD

- 8. How to Check Servo Operation
- (1) How to Check Focus Search and Spindle kick.
 - If PLAY key is pressed without inserting a disk. Can check Focus Search and Spindle kick. Repeat until press. STOP key?

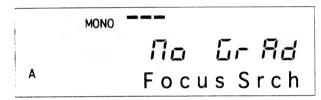


fig-4

If PLAY key is pressed with a disk inserted, Focus search and Spindle kick become on.

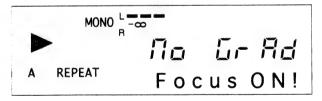


fig-5 MO Disk



fig-6 CD Disk

- (2) How to Check ALL Servo ON
 - 13 After completing step (1) (Focus Search and Spindle Kick) operation, press MODE key, Tracking gain and Sled servo become ON, and all Servo will start then Display of LCD shows Disk Address and audio signal output is 1kHz.
 - 2) To press the MODE key after above operation 1), Auto gain will works.

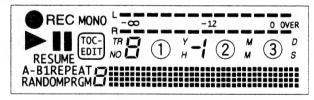


fig-7 MO Disk



fig-8 Alumunume (CD) Disk

* This mode will shows Disk type (MO or Aluminum (CD)) and condition on LCD

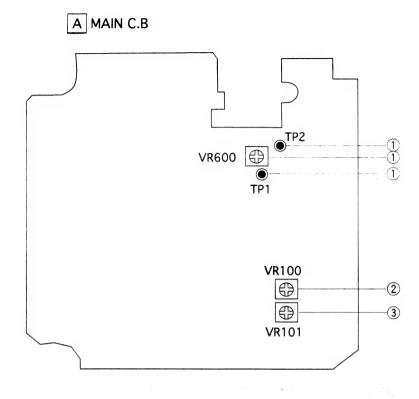


		1	2	3
МО	Pit	По	P,	EF
MO	Groove	По	5-	Ad
	AL	AL	P,	EF

fig-9

Disk type MO (can recording) or AL (Aluminum (CD)

2 Track kinds (type) Groove or Pit 3 LCV servo EFM or ADIP



*Establish the test mode to perform the following adjustments.

1. Power Supply Check

Setting: · Test point:

oint: TP(1),(2)

· Adjustment point :VR600

Method: Adjust VR600 so that the voltage between TP1 and

TP2 becomes 3.0 ± 0.05 V.

2. Laser Power Adjustment

Setting: Test point :Laser output of pick up

· Adjustment location : VR100

Method: Use a laser power meter. Adjust VR100 so that the laser power is 3.40±0.50mW in the 1/2 MO-W mode.

After Adjust 1/2 MO-W so that the laser power is 6.80

± 0.10mW in the MO-W mode.

With care during adjustments, if so pick-up get damage when laser output is more than 7.00mV.

3. FE Balance Adjustment

Setting: Test point: Check the Display of LCD

· Adjustment location : VR101

· Test Disk :SONY MDW-60 (MQ)

Method: Turn off the HOLD SW.

2 Move pick up the most inside to a little outside.

(Press key, and move outside.)

3 Press the "PLAY" key " → "EDIT" key →

"DISP" key

4 Adjust VR101 so that the display of LCD changes around a center point at \$80.

Caution: A range of Moving numbers are between \$71 and \$8E

4. Focus / Tracking /Thread Gain Check

Setting: • Test Disk :SONY TGYS-1 (CD)

Method: Î Turn on HOLD SW.

2 Press the "PLAY" key → "MODE" key →

"MODE" key →"STOP" key.

3 Press the "DISP" key 3 times.

4 Check display of LCD is limit by the following.

Gf <u>1</u> t <u>2</u> s <u>3</u>

1 Focus Gain : 21~43
 2 Tracking Gain : 10~25

3 Thread Gain: 10~25

* Thereafter items are not usually use at adjustments and checks.

5 Error Rate

5 - 1 MO error Rate Check

Setting: Test Disk: SONY MDW-60 (MO)

Method: Turn off HOLD SW.

2 Move pick up the most inside to a little outside.

(Press key, and move outside.)

3 Press the "VOLUME (- " key and down the

OWH. (Over Write Head.

(Display "Toc-EDIT" the LCD.

4 Press the "REC" key → "PLAY" key.

Display "Focus ON" the LCD.

5 Press the "MODE" key twice.

(Display "ALL SV ON" the LCD.)

6 Press the "REC" key once more, and start at 600

cluster.

(LCD display is start at "600".)

Z Record about 15 second.

18 Press the "STOP" key.

(Cluster Go back recording start position, and

stop.)

9 Press the "VOLUME + " key and up the OWH.

(Over Write Head .)

(Display "MONO" the LCD.)

5 - 2 Error rate Check

Setting: · Test Disk :SONY MDW-60 (MO)

Method: ①After recording, press the "PLAY" key →

"MODE" key →"DISP" key.

2 Check display less than "Err 0030" of LCD.

5 - 3 MO PIT Playback error Rate Check

Setting: Test Disk: SONY MDW-60 (MO)

thod: 1 Move pick-up to the most inside.

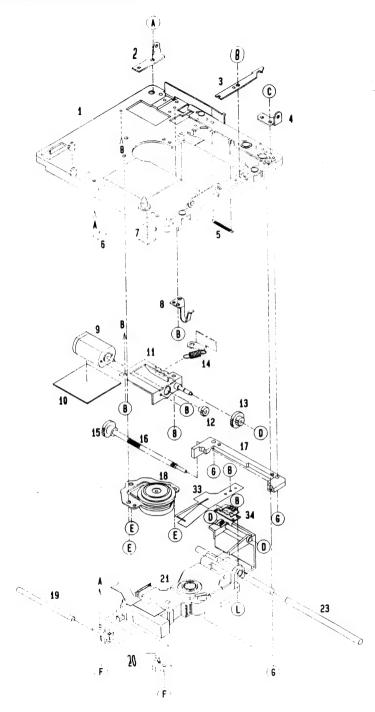
(Press the " key.)

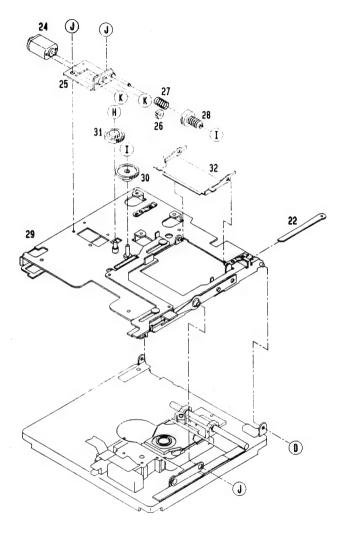
2 Turn on the "HOLD" key.

3 Press the "PLAY" key →"MODE" key →"DISP"

key.

4 Check display less than "Err 0030" of LCD.

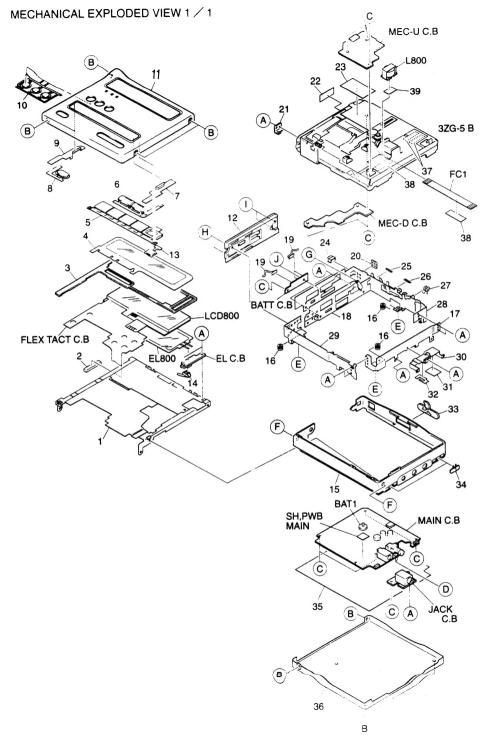


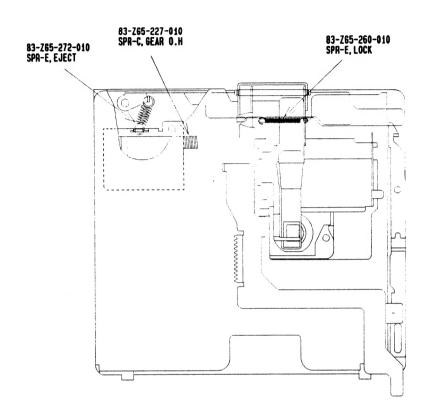


MECHANICAL PARTS LIST 1 / 1

DESCRIPTIONで判断できない物は "REFERENCE NAME LIST" を参照してください。 If can't understand for Description please kindly refer to "REFERENCE NAME LIST".

IEF. NO	PART NO.	カンリ DESCRIPTION NO.	REF. NO	PART NO.	カンリ DESCRIPTIO NO.
	85-HM1-201-010			85-HM1-229-01	
	85-HM1-250-010			85-HM1-240-01	
	85-HM1-225-010			85-HM1-235-31 85-HM1-221-01	
	85-HM1-004-010				
5	85-HM1-006-010	KEY, CONT MAIN	30	85-HM1-223-01	U HLDK, JACK
6	85-HM1-007-010	KEY, CONT VOL		85-HM1-231-01	
7	85-HM1-012-010	GUIDE, CONT R		85-HM1-232-01	
8	85-HM1-009-010	KEY, CONT DISP	33	85-HM1-013-11	.0 KNOB, SL EJECT
9	85-HM1-011-010	GUIDE, CONT L	34	85-HM1-017-01	.0 KNOB, SL A
10	85-HM1-008-01	KEY, CONT INDEX	35	85-HM1-233-01	.0 SH, REAR
11	85-HM1-001-01) PANEL, TOP	36	85-HM1-002-01	.0 PANEL, BOT <ahe1></ahe1>
	85-HM1-005-11			85-HM1-018-01	
	85-HM1-010-010		37	85-HM1-246-01	.0 SH, ADHESIVE FLK
	85-HM1-227-010		38	85-HM1-245-91	.0 SH, ADHESIVE TR
	85-HM1-003-01		39	85-HM1-251-11	.0 CUSH, TR
16	85-HM1-224-01	DMPR,G	A	87-067-393-01	
	85-HM1-220-01		В	87-078-214-01	
18			С	87-067-746-01	10 SCREW-SERR M1.4-3
19			D	87-067-871-01	LO S-SCREW, 1.4-3.513
20			Ε	85-HM1-234-01	10 S-SCREW, DAMPER
21	85-HM1-222-01	DMPR.PLATE	F	87-067-388-03	
	85-HM1-248-01		G	87-078-213-03	LO S-SCREW, 1.4-0.4-1
	85-HM1-242-01		н	87-067-494-03	LO V+1.7-4
24			I	85-HM1-239-03	LO S-SCREW, 1.7-2.0 E
25			Τ.	87-067-589-03	10 V+1.4-1.4 BLK NLC





MD MECHANISM PARTS LIST 1 / 1

DESCRIPTIONで判断できない物は "REFERENCE NAME LIST" を参照してください。 If can't understand for Description please kindly refer to "REFERENCE NAME LIST".

REF. NO	PART NO.	カンリ NO.	DESCRIPTION	REF. NO	PART NO.	カン! NO.	
	83-ZG5-280-010		CHAS, B ASSY		83-ZG5-222-010		GEAR, MOTOR O.H.
	83-ZG5-255-010		PLATE, PIVOT A		83-ZG5-227-110		SPR-C,GEAR O.H.
	83-ZG5-271-010		SPR-P, EJECT		83-ZG5-223-010		GEAR, O.H.C
	83-ZG5-256-010		PLATE, PIVOT B		83-ZG5-288-010		HLDR, CTRG B ASSY
5	83-ZG5-260-010		SPR-E, LOCK	30	83-ZG5-220-010		GEAR, O.H.A
6	87-085-246-110		SH, 5-3.5-0.05	31	83-ZG5-221-010		GEAR, O.H.B
7	87-085-245-110		SH,5-5-0.2	32	83-ZG5-216-010		LVR, KICK O.H.
8	83-ZG5-265-010		SPR-P, GEAR P.U.	33	37-046-415-010		HEAD, RF320-74H
9	87-045-374-010		MOT, FFM20VK-72170	34	93-ZG5-247-110		SPR-P, INSERT ASSY
10	83-ZG5-277-010		SH,20-12-0.05	A	37-232-501-310		Q+1.4-1.6 BLK
11	83-ZG5-261-010		HLDR, MOTOR P.U.ASSY	В	37-261-500-313		V+1.4-1.4 BLK(1)
12	83-ZG5-268-010		GEAR, MOTOR P.U.	2	37-237-501-313		0+1.4-1.6 GLD
13	83-ZG5-267-010		GEAR, P.U.B	٥	37-078-123-013		PW,1.1-2.5-0.3 C
14	83-ZG5-272-010		SPR-E, EJECT	Ξ	37-262-521-310		V+1.7-1.6 BLK
15	83-ZG5-266-010		GEAR, P.U.A	F	37-262-505-310		V+1.4-2.5 BLK(3)
16	83-ZG5-254-110		SHAFT, PU GUIDE C	g	87-262-507-310		V+1.4-3.0 BLK
17	93-ZG5-253-110		HLDR, PU GUIDE B		87-067-676-013		PW1.1-2.5-0.3CUT
18	87-045-373-110		MCT, SPINDOL '	:	37-067-569-010		PW, 0.33-2.5-0.25 SI
19	83-ZG5-250-010		SHAFT, P.U.GUIDE A	3	37-262-500-310		V+1.4-1.4 BLK
20	83-ZG5-252-110		HLDR, PU GUIDE A	K	87-078-120-010		1V+1.2-1.5
21	83-ZG5-297-010		PICK UP ASSY, 2	i.	87-262-523-313		V+1.7-2 BLK(3)
22	83-ZG5-204-010		SPR-P, KICK				
23	83-ZG5-251-010		SHAFT, P.U.GUIDE B				
24	87-045-375-010)	MOT, LA8-388				
25	83-ZG5-224-010)	HLDR, MOTOR O.H.ASSY				

■ ACCESSORIES / PACKAGE LIST

DESCRIPTIONで判断できない物は "REFERENCE NAME LIST" を参照してください。 If can't understand for Description please kindly refer to "REFERENCE NAME LIST".

REF. NO	PART NO.	カンリ NO.	DESCRIPTION
	84-HM1-627-010 84-HM1-624-010 85-HM1-952-010 84-HM1-629-110 85-HM1-954-010	AC BA BA	-421 E <ae1<h>> -421 H<ahe1<h>> G, CARRING TT, LIF-12 XX ASSY, BAT</ahe1<h></ae1<h>
	87-050-049-010 85-HM1-951-010 85-HM1-956-010 85-HM1-903-010 87-009-725-010	HE IB	ORD, 1M 3.5-PIN S OVER, BAT RADPHONE, HP-RMF3 B, EX (J) UUG, ADPTR, IR40 <ahe1<h>>></ahe1<h>